

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yasunobu TAGUSA	Art Unit: 1714
Application No.: 10/598,488	
Confirmation No.: 6247	Examiner: G. Rao
Filed: August 31, 2006	
Title: METHOD FOR FABRICATING A SEMICONDUCTOR DEVICE AND APPARATUS FOR INSPECTING A SEMICONDUCTOR	

REQUEST FOR RECONSIDERATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated May 25, 2011, please reconsider the above-identified application in view of the following remarks.

Listing of the Claims begins on page **2**. No amendments have been made to the Claims.

Remarks/Arguments begin on page **5** of this paper.